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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/613,553	07/02/2003	Stefano S. Oggioni	FR920020056US1	6676
26502	7590	12/13/2005	EXAMINER	
IBM CORPORATION IPLAW IQ0A/40-3 1701 NORTH STREET ENDICOTT, NY 13760			NORRIS, JEREMY C	
			ART UNIT	PAPER NUMBER
			2841	

DATE MAILED: 12/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

**Application No.**

10/613,553

**Applicant(s)**

OGGIONI ET AL.

**Examiner**

Jeremy C. Norris

**Art Unit**

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 13 September 2005.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 15-19 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 15-19 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 March 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
  - 2) ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 13 September 2005 has been entered.

### ***Claim Objections***

Claims 15- are objected to because of the following informalities: Claim 15 states the phrase "dielectric core to a second", which is grammatically awkward. Appropriate correction is required. Examiner suggests changing to --dielectric core, a second--. Examiner has used this interpretation for examination.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 15-19 are rejected under 35 U.S.C. 102(e) as being anticipated by US 6,847,274 (Salmela).

Salmela discloses, referring to figures 1-3, a coaxial via structure in an electronic device carrier adapted to connect power/ground voltage comprising, a first conductive track of a first conductive layer on a first surface of a dielectric core (in figure 3, the middle conductive track on the third conductive layer from the top), a second conductive track on a second conductive layer in a central portion of dielectric core (middle tracks on the fourth layer from the top), and a third conductive track of a third conductive layer (middle tracks, fifth layer from the top), said conductive layer being separated by dielectric layer (5); a coaxial via structure having at least two first conductive vias for conducting voltage, only connected to said first and second conductive tracks (see figures 2 and 3), second conductive vias for conducting voltage, only connected to said second and third conductive tracks (see figures 2 and 3), a fourth conductive track of a fourth conductive layer surrounding said first conductive layer (outer tracks, third layer from top); a fifth conductive track of a fifth conductive layer surrounding said second conductive layer (outer tracks, fourth layer from top), a sixth conductive track of a sixth conductive layer surrounding said third conductive layer (outer tracks, fifth layer from top), at least two third conductive vias for conducting voltage, only connected to said fourth and fifth conductive tracks (see figures 2 and 3); and at least two fourth conductive vias for conducting voltage, only connected to said fifth and sixth conductive tracks (see figures 2 and 3) [claim 15], wherein there are at least four vias of either the first set of vias or the second set of vias (see figure 2) [claim 16], wherein the vias of at least one set are symmetrically arranged with respect to each other (see figure 3) [claim

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17], wherein the vias of each set are symmetrically arranged with respect to each other (see figure 3) [claims 18, 19].

### ***Response to Arguments***

Applicant's arguments with respect to claims 15-19 have been considered but are moot in view of the new ground(s) of rejection.

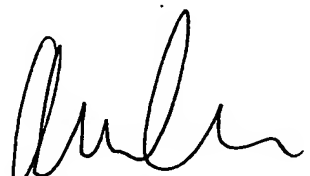
### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeremy C. Norris whose telephone number is 571-272-1932. The examiner can normally be reached on Monday - Friday, 9:30 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JCSN

  
KAMAND CUNEO  
SUPERVISORY PATENT EXAMINER  
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